



- Package Size: 3.2\*2.5, 5.0\*

Frequency Range (额定频率范围)

Supply Voltage (电源电压)

Output Type (输出方式)

Frequency Tolerance (频率偏差) (at 25°C)

Output Load

### Symbol

$f_0$

VDD

-

f\_tol

### Specifications

1MHz ~ 100MHz, or specify

5V

CMOS

$\pm 10\text{ppm}$ ,  $\pm 20\text{ppm}$ , or specify

on 15pF only



