



Package Size: 2.5\*2.0, 3.2\*2.5, 5.0\*3.2  
7.0\*5.0mm

## Applications

- Optical modules
- Accelerator Card

## Features

- Frequency Range: 13.5MHz ~ 200MHz
- Output: LVPECL, HCSL or LVDS

Item/Type	LVPECL		LVDS		HCSL		Remarks
	7050/5032	3225/2520	7050/5032	3225/2520	7050/5032	3225/2520	
Output Frequency Range	13.5 ~ 200 额定频率范围	13.5 ~ 156.25 M-HZ	13.5 ~ 200 M-HZ	13.5 ~ 156.25 M-HZ	13.5 ~ 200 M-HZ	13.5 ~ 156.25 M-HZ	M-HZ
Supply Voltage 电源电压	2.5V ~ 3.3V		1.8V, 2.5V ~ 3.3V		2.5V ~ 3.3V		
Operating Temperature 工作温度	-40 ~ +85°C, 100% humidity						
Storage Temperature 储存温度	-55 ~ +125°C						
Total Stability 频率偏差	±25ppm ±50ppm ±20ppm ±20ppm ±20ppm ±20ppm						Inclusive of Initial tolerance at 25°C, 1st and variations over operating temperature and variations over 10 years aging at 25°C
Current Consumption	80mA Max	50mA Max	60mA Max	40mA Max	50mA Max	50mA Max	OE=VDD, LVPECL=(50)Ω, HCSL=(50)Ω or LVDS=(100)Ω
Disable Current 禁用电流	10mA Max						
Output Voltage (LVPECL) 输出电压 (LVPECL)	VOH=VDD-1.05V Min VOL=VDD-1.6V Max						
Output Voltage (LVDS) 输出电压 (LVDS)	VOH=1.6V Max VOL=0.9V Min						
Output Voltage (HCSL) 输出电压 (HCSL)	VOH=0.9V Max VOL=0.15V Min						
Output Load Condition	I <sub>L</sub> LVDS=100Ω						
Input Voltage 输入电压	VIH=70% VDD Min, VIL=30% VDD Max						
Duty Cycle 占空比	45~55%						
Rise Time/Fall Time 上升/下降时间	@3ns Max						
Start-up time 启动时间	10ms						
Aging 老化	±3ppm						
Frequency	156.25MHz	180MHz	200MHz	20MHz	100MHz	125MHz	148.5MHz
Timing	@3ps 15ps						





A capacitor of value 0.01uf-0.1uf or higher between VDD and GND is required.

Notes:  
1. A capacitor of value

(测试电路)

Test Circuit

